



N THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Akram et al.

Serial No.: 09/653,272

Filed: August 31, 2000

For: METHOD FOR USING DATA REGARDING MANUFACTURING PROCEDURES INTEGRATED CIRCUITS (IC'S) HAVE UNDERGONE, SUCH AS REPAIRS, TO SELECT PROCEDURES THE IC'S WILL UNDERGO, SUCH AS

ADDITIONAL REPAIRS

Confirmation No.: 4727

Examiner: S. Garland

Group Art Unit: 2125

Attorney Docket No.: 2269-4181US

(96-0973.02/US)

NOTICE OF EXPRESS MAILING

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO/SB/08 be considered by the Examiner and made of record. Pursuant to M.P.E.P. 609 III A(2), copies of U.S. patents

are <u>not</u> being provided. Copies of foreign patent documents and non-patent literature are enclosed pursuant to 37 C.F.R. § 1.98(a)(2).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

U.S. Patent Documents

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U.S. Patent No.	Publication Date	<u>Patentee</u>
4,027,246	05/1977	Caccoma et al.
4,150,331	04/1979	Lacher
4,454,413	06/1984	Morton, Jr.
4,985,988	01/1991	Littlebury
5,043,657	08/1991	Amazeen et al.
5,118,369	06/1992	Shamir
5,175,774	12/1992	Traux et al.
5,197,650	03/1993	Monzen et al.
5,347,463	09/1994	Nakamura et al.
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5,360,747	11/1994	Larson et al.
5,399,531	03/1995	$\mathbf{W}\mathbf{u}$
5,448,488	09/1995	Oshima
5,654,204	08/1997	Anderson
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5,837,558	11/1998	Zuniga et al.
5,890,807	04/1999	Ingel et al.
5,994,915	11/1999	Farnworth et al.
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6,265,232 B1	07/2001	Simmons
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6,424,168 B1	07/2002	Farnworth et al.
6,534,785 B1	03/2003	Farnworth et al.
6,613,590 B2	09/2003	Simmons
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	•	'

Foreign Patent Documents

Document No.	Publication Date	<u>Patentee</u>
JP 02246312 A	10/02/1990	Fujitsu Ltd.
JP 04080949 A	03/13/1992	Hitachi Ltd.
JP 04318911 A	11/10/1992	Matsushita Electric
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JP 05315207	11/26/1993	NEC Corp.
JP 06013443 A	01/21/1994	Toshiba Seiku KK
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JP 07050233	02/21/1995	Kawasaki Steel
JP 11008327	01/12/1999	Sony Corp
JP 58052814 A	03/29/1983	NEC Corp.
	Other Documents	•

Fresonke, Dean, *In-Fab Identification of Silicon Wafers with Clean, Laser Marked Barcodes*, Advanced Semiconductor Manufacturing Conference and Workshop, 1994, IEEE/SEMI, pp. 157-60.

Applicants offer to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is believed to be filed before the mailing date of the first Office Action on the merits subsequent to the filing of an RCE in the above-identified application, and therefore no fee is due.

Respectfully submitted,

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Registration No. 28,393

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Date: November 2, 2005

JRD/ps:lmh

Enclosures: Form PTO/SB/08

Copy of non-U.S. Patent documents cited----

Document in ProLaw

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

Complete if Known			
Application Number	09/653,272		
Filing Date	August 31, 2000		
First Named Inventor	Akram et al.		
Group Art Unit	2125		
Examiner Name	S. Garland		
Attorney Docket Number	2269-4181US (96-0973.02/US)		

	T	Document Number		Name of Patentee or Applicant of	
Examiner Initials *	Cite No.	Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		4,027,246	05/1977	Caccoma et al.	
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		5,360,747	11/1994	Larson et al.	
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	T	5,448,488	09/1995	Oshima	
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		JP 02246312 A	10/02/1990	Fujitsu Ltd.		X
		JP 04080949 A	03/13/1992	Hitachi Ltd.		X
		JP 04318911 A	11/10/1992	Matsushita Electric		X
		. JP 05121573 A	05/18/1993	Sharp Corp		X
		JP 05315207	11/26/1993	NEC Corp.		X
		JP 06013443 A	01/21/1994	Toshiba Seiku KK		X
		JP 06349691 A	12/22/1994	Hitachi Ltd.		X
		JP 07050233	02/21/1995	Kawasaki Steel		X
		JP 11008327	01/12/1999	Sony Corp		X

Examiner	Date
Signature	Considered

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		6,424,168 B1	07/2002	Farnworth et al.			
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		JP 58052814 A	03/29/1983	NEC Corp.		X	
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 Attorney Docket Number
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		Fresonke, Dean, In-Fab Identification of Silicon Wafers with Clean, Laser Marked Barcodes, Advanced Semiconductor Manufacturing Conference and Workshop, 1994, IEEE/SEMI, pp. 157-60.	
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